

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hirobumi FURIHATA et al.
Title: CONTROLLER DRIVER AND DISPLAY APPARATUS
USING THE SAME
Appl. No.: 10/561,270
International Filing Date: 6/28/2004
371(c) Date: 1/23/2006
Examiner: Vinh Tang Lam
Art Unit: 2629
Confirmation Number: 5886

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to Applicants in order to comply with Applicants' duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The instant Information Disclosure Statement is being filed after the mailing date of the final action under 37 CFR §1.113. Accordingly, pursuant to 37 CFR §1.97(d), a statement and fee are required.

RELEVANCE OF EACH DOCUMENT

The documents listed on the attached PTO/SB/08 were cited as being relevant during the prosecution of the corresponding Japanese application. A copy of the Japanese Office Action dated July 21, 2010 is attached herewith. A partial English translation of the Japanese Office Action follows:

Note (for the cited literature, see the List of Cited Literature)

- Claims 1 and 3
- Reasons 2 and 3
- Cited Literature 1

Cited Literature 1 describes the invention of a controller driver provided with a first memory 115, a first control circuit 112 that performs compound processing on compression-encoded data that has been received from outside and stores the compound-processed image data in the first memory 115, a memory unit 120 that receives image data generated from the image data stored in the first memory 115 and stores it, and a data line drive circuit 117 that receives the image data stored in the memory unit 120 from the memory unit 120 and drives a display panel in response to the image data received from the memory unit 120 (see (0009) to (0010), (0059) to (0061), and Figure 1).

Thus, the inventions according to Claims 1 and 3 do not differ from the invention described in Cited Literature 1. Moreover, the inventions according to Claims 1 and 3 could be easily conceived of by a person skilled in the art based on the invention described in Cited Literature 1.

- Claim 2
- Reason 3
- Cited Literature 1 and 2

Cited Literature 2 describes the invention of a display device characterized in that graphic information for display in vector format that has been input is converted into raster format and stored in a frame buffer memory 7 (see line 11 of the upper left section of page 2 to line 6 of the upper right section of the same page and Figure 5).

Since the invention described in Cited Literature 1 and the invention described in Cited Literature 2 have in common the point that they are inventions of display devices provided with memories, creating the invention according to Claim 2 by giving the first control circuit 112 in the invention described in Cited Literature 1 the function of converting image data in vector format into raster format could be easily conceived of by a person skilled in the art.

List of Cited Literature

1. Japanese Unexamined Patent Application Publication 2002-091332
2. Japanese Unexamined Patent Application Publication S64-013585

Record of Prior Art Literature Search Results

- | | | | | |
|-------------------------|-------------|------------|--------|--|
| • Fields searched | IPC | G09G 3/20 | | |
| | G09G 5/00 | | | |
| • Prior art literature | Japanese | Unexamined | Patent | |
| Application Publication | 2003-108091 | | | |

This Record of Prior Art Literature Search Results does not constitute a reason for rejection.

Documents A1 and A2 are U.S. counterparts of document A5.

Documents A3 and A4 are U.S. counterparts of document A7.

Applicants respectfully request that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

STATEMENT

The undersigned hereby states in accordance with 37 CFR §1.97(e)(1) that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to filing of this Statement.


FEE

Fees in the amount of \$180.00 to cover the fee associated with an information disclosure statement under 37 CFR §1.97(d) are being paid by credit card via EFS-Web.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this submission under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment instructions in EFS-Web being incorrect or absent, resulting in a rejected or incorrect credit card transaction, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date September 23, 2010

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